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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/720,076	11/25/2003	Yung-Chien Lee	0698-0168P	4937
2292	7590	06/23/2005	EXAMINER	
BIRCH STEWART KOLASCH & BIRCH PO BOX 747 FALLS CHURCH, VA 22040-0747			TSAI, CAROL S W	
			ART UNIT	PAPER NUMBER
			2857	

DATE MAILED: 06/23/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/720,076

Applicant(s)

LEE, YUNG-CHIEN

Examiner

Carol S. Tsai

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 2 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 09 January 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☒ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-9 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-9 is/are allowed.
- 6) ☐ Claim(s) _____ is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 25 November 2003 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Drawings

1. The drawings are objected to under 37 CFR 1.83(a) because the blank boxes shown should be labeled as to their function, for example: elements 13 and 21 in Fig. 1, as described in the specification. Any structural detail that is essential for a proper understanding of the disclosed invention should be shown in the drawing. MPEP § 608.02(d). Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as “amended.” If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either “Replacement Sheet” or “New Sheet” pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Conclusion

2. This application is in condition for allowance except for the following formal matters:

The objection to the Drawings.

Prosecution on the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

A shortened statutory period for reply to this action is set to expire **TWO MONTHS** from the mailing date of this letter.

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Blyth discloses an automatic test system for testing and diagnosing faults in a printed circuit board carrying electronic components.

Hogan, Jr. et al. disclose a testing arrangement in which an AI system is interfaced directly to an automatic test system (ATS) such that from the point of view of the ATS, the actions of the AI system are indistinguishable from the actions of an intelligent human operator.

Bald et al. disclose a leakage tester including multiple circuits capable of simulating the impedance of the human body to electrical current under a variety of conditions, and in which the multiple circuits can be selected using a single menu-based operator interface for testing both normal-line and single-fault conditions such as open-neutral, reversed-line, and open-ground conditions, as well as for performing additional types of safety compliance tests including dielectric withstand, insulation resistance, and ground bond or continuity tests, in any sequence.

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Bald et al. disclose a safety compliance test instrument including circuitry for performing insulation resistance and ground circuit tests such as a ground bond test, as well as high voltage AC and DC dielectric withstand or hipot tests.

Fredrickson discloses a semiconductor device tester and handler interface including tester and handler boards with a coplanarity plate between them.

Allowable Subject Matter

4. Claims 1-9 are allowed.

5. The following is a statement of reasons for the indication of allowable subject matter:

U.S. Patent No. 6,453,435 to Limon, Jr. et al. in view of U.S Patent No. 6,815,956 to Weldin et al. are references closest to the claimed invention. Limon, Jr. et disclose a method of operating a test station which can be detachably operatively coupled to a test unit to be tested, including: a coupling portion operative to facilitate a detachable operative coupling of the test station to a test unit which has indicia thereon; a reader portion which can read the indicia on the unit; and a control portion which is operatively coupled to the coupling portion and the reader portion, the control portion being responsive to information obtained through the reader portion for carrying out through the coupling portion at least one operation which is selected as a function of the information; storing a test definition; and causing the test station to respond to the test definition by automatically carrying out on the test unit a sequence of test operations specified by the test definition, and by presenting on a display a graphic depiction of how to perform a manual step required by the test definition. Weldin et al. disclose an apparatus and method used to perform automatic high potential (hi-pot), megohmmeter and continuity, circuit

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test which includes a power source connected to a central processing unit (cpu) having a floppy drive wherein a data entry device is connected to the cpu for providing input, a hard drive, an analog to digital (A/D) printed circuit board (pcb), and a predetermined number of digital I/O pcb's, a hi-pot device which is connected to the A/D pcb and to a digital I/O pcb for providing an input voltage of a predetermined magnitude, and a power supply wherein a multiplexer is connected to the power supply and to a digital I/O pcb and the hi-pot device for communicating the input voltage to a predetermined number of external circuits to provide testing of the external circuits. However, Limon, Jr. et in combination with Bald et al. do not teach an automated test method for testing voltage tolerance of an electrical device via an operating system connected to and operative with a test instrument, the operating system being connected via one of a first connection port and a second connection port thereof to the testing instrument to perform a hi-pot test for the electrical device, the method comprising the steps of: (1) having the operating system read and verify a serial number of the electrical device to store the serial number as a file in a predetermined folder of the operating system if the serial number is correct; (2) having the operating system urge a test program installed therein to read the stored file of the serial number of the electrical device; (3) establishing via the test program a connection between the test instrument and the first connection port of the operating system, and sending a control command to the test instrument via the connection between the operating system and the test instrument, so as to allow the test instrument to perform the hi-pot test for the electrical device; and, (4) having the test program read test results of the electrical device from the test instrument using the control command, and store the test results in the predetermined folder of the operating system; and including all of the other limitations in the respective independent claims.

Contact Information

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Carol S. W. Tsai whose telephone number is (571) 272-2224. The examiner can normally be reached on Monday-Friday from 8:30 AM to 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Marc S. Hoff can be reached on (571) 272-2216. The fax number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 886-217-9197 (toll-free).



Carol S. W. Tsai
Primary Examiner
Art Unit 2857

Cswt
June 16, 2005